Reg. No.				

## G. VENKATASWAMY NAIDU COLLEGE (AUTONOMOUS), KOVILPATTI - 628 502.



## **UG DEGREE END SEMESTER EXAMINATIONS - APRIL 2025.**

(For those admitted in June 2023 and later)

## PROGRAMME AND BRANCH: B.Sc., ELECTRONICS

SEM	CATEGORY	COMPONENT	COURSE CODE	COURSE TITLE
I	PART-III	CORE - 1	U23EL101	ELECTRONICS DEVICES

Date	& Sessi	on: 23	.04.2025/AN	Time: 3 hours	Maximum:	75 Marks	
Course Outcome	Bloom's K-level	Q. No.	<u>SECTION - A (10 X 1 = 10 Marks)</u> Answer <u>ALL Questions.</u>				
CO1	K1	1.	The forward voltage drop a) 0.3V b) 0V			.62V	
CO1	K2	2.	A Zener diode is used as a) an amplifier c) a rectifier	b) a volta	ige regulator ivibrator		
CO2	K1	3.	Which of the following is a) BJT b) UJ	_	d) M	MOSFET	
CO2	K2	4.	The α of a transistor sho a) 1 b) <1	ould be.	d) 0	.1	
CO3	K1	5.	A JFET has three terming a) Cathode, anode, grid c) source, gate, drain	b) emitte	er, base, collector source, gate		
CO3	K2	6.	What is the full form UJ a) Uni Junction Transist c) Union Junction Trans	tor b) Unive	rsal Junction Transis rm Junction Transist		
CO4	K1	7.	SCR is abbreviated as a) Silicon controlled rect c) Silicon-controlled resi	ifier b) Silicon	conducting rectifier conducting resistor		
CO4	K2	8.	A MOSFET has t a) Two b) five	erminals.		three	
CO5	K1	9.	LED full form is  a) Lightly Emitting Diod c) Light Effect Diode	le b) Light	Emitting Diode Effect Diode		
CO5	K2	10.	What is full form of LCD a) Liquid catalogue displ c) Liquid crystal data	ay b) Liquid	l crystal display l code display		
Course Outcome	Bloom's K-level	Q. No.	$\frac{\text{SECTION} - B \text{ (5 X 5 = 25 Marks)}}{\text{Answer } \underline{\text{ALL }} \text{Questions choosing either (a) or (b)}}$				
CO1	КЗ	11a.	Define an intrinsic semiconductor with a neat sketch.  (OR)				
CO1	КЗ	11b.	How does a Zener diode act as a voltage regulator?				

CO2	КЗ	12a.	Illustrate the working principle of the NPN transistor. (OR)
CO2	КЗ	12b.	Describe shortly about the common collector configuration.
CO3	K4	13a.	Discuss the constructional details of JFET. (OR)
CO3	K4	13b.	Discuss the constructional details of UJT.
CO4	K4	14a.	Draw the V-I characteristics curve of a SCR and explain it. (OR)
CO4	K4	14b.	Draw the symbol of N-Channel Enhancement MOSFET and explain its working principle.
CO5	K5	15a.	What are the characteristics of LDR? (OR)
CO5	K5	15b.	What are the various digits that can be displayed using a 7-segment LED?

Course	Bloom's K-level	Q. No.	$\frac{\text{SECTION} - C \text{ (5 X 8 = 40 Marks)}}{\text{Answer } \frac{\text{ALL}}{\text{Questions choosing either (a) or (b)}}$
CO1	КЗ	16a.	Describe the two working principles of PN junction diode with a neat diagram. (OR)
CO1	КЗ	16b.	Draw the characteristic curve of a Zener diode and explain it.
CO2	K4	17a.	How does a transistor work under the common base configuration? (OR)
CO2	K4	17b.	Detail the voltage divider biasing of a transistor.
CO3	K4	18a.	What would happen if the gate closed in the JFET circuit? (OR)
CO3	K4	18b.	How does UJT act as a relaxation oscillator?
CO4	K5	19a.	What are the two main components of the SCR's operational principle? Explain it with a neat diagram.  (OR)
CO4	K5	19b.	Draw the operational diagram of a MOSFET and expound it.
CO5	K5	20a.	How does a phototransistor convert a light into an electrical signal? (OR)
CO5	K5	20b.	Explain the concept of a photodiode with a neat sketch.